

Electronic Acknowledgement Receipt

EFS ID:	1059574
Application Number:	09849822
Confirmation Number:	8898
Title of Invention:	MICROPROCESSOR WITH SELECTIVELY OVERRIDING FIRST AND SECOND PREDICTIVE CALL/RETURN STACKS
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part	Pages
1	Information Disclosure Statement (IDS) Filed	CNTR_2050_IDS.pdf	39792	no	3
Warnings:					
Information:					
This is not an USPTO supplied IDS fillable form					
2	NPL Documents	Computer_Organization_and_Design.pdf	328842	no	3
Warnings:					
Information:					
3	NPL Documents	Computer_Organization_Design_pages_453-455.pdf	320273	no	4
Warnings:					
Information:					
4	NPL Documents	Online_Computing.pdf	95807	no	1
Warnings:					
Information:					
5	NPL Documents	The_Correlation_Branch.pdf	167985	no	3
Warnings:					
Information:					
6	NPL Documents	The_D_Latch.pdf	100404	no	1
Warnings:					
Information:					
7	NPL Documents	The_Impact_of_Delay.pdf	806756	no	10
Warnings:					
Information:					
8	Fee Worksheet (PTO-875)	fee-info.pdf	8220	no	2

Warnings:	
Information:	
Total Files Size (in bytes):	1868079
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